10 November 2015

ASX Release

BrainChip Strengthens Scientific Advisory Board - Appointment of Dr. Gert Cauwenberghs

BrainChip Holdings Limited ("BrainChip" or "the Company") is pleased to advise of the appointment of Dr. Gert Cauwenberghs to our Scientific Advisory Board.

Peter van der Made CTO and Interim CEO of BrainChip said, "We are very pleased to have Dr. Cauwenberghs join our Scientific Advisory Board. He is a very accomplished scientist, with a focus on a number of key neural disciplines that will broaden our knowledge base and reach into both academia and the commercial world."

Biography

Professor Cauwenberghs pioneered the design and implementation of highly energy efficient, massively parallel microchips that emulate function and structure of adaptive neural circuits in silicon. Embedded mechanisms of synaptic plasticity in these silicon microcircuits model the adaptive intelligence of biological nervous systems interacting with variable and unpredictable environments, and assist in optimizing the energy efficiency and noise robustness of nanoscale circuit components implementing the neural functions. Recently the Cauwenberghs group demonstrated synaptic arrays in silicon for adaptive template-based visual pattern recognition operating at less than a femtojoule of energy per synaptic operation, exceeding the nominal energy efficiency of synaptic transmission in the human brain.

Gert Cauwenberghs received the M.Eng. degree in applied physics from University of Brussels, Belgium, in 1988, and the M.S. and Ph.D. degrees in electrical engineering from California Institute of Technology, Pasadena, in 1989 and 1994. He is Professor of Bioengineering at University of California San Diego, where he co-directs the Institute for Neural Computation. Previously, he held positions as Professor of Electrical and Computer Engineering at Johns Hopkins University, Baltimore Maryland, and as Visiting Professor of Brain and Cognitive Science at Massachusetts Institute of Technology, Cambridge.

He is a Francqui Fellow of the Belgian American Educational Foundation, and received the National Science Foundation Career Award in 1997, Office of Naval Research Young Investigator Award in 1999, and Presidential Early Career Award for Scientists and Engineers (PECASE) in

2000. He is a Fellow of the Institute of Electrical and Electronic Engineers (IEEE) and of the American Institute of Medical and Biological Engineering (AIMBE). He was Distinguished Lecturer of the IEEE Circuits and Systems Society in 2003-2004, and chaired its Analog Signal Processing Technical Committee in 2001-2002. He was Senior Editor for the IEEE Sensors Journal, and currently serves as Editor-in-Chief for IEEE Transactions on Biomedical Circuits and Systems, and on the Editorial Board of IEEE Transactions on Biomedical Engineering.

The Institute for Neural Computation (INC) is an organized research unit of the University of California at San Diego <http://www.ucsd.edu/> with 44 members representing 14 research disciplines, devoted to the research and development of a new generation of massively parallel computers through a coherent and cohesive plan of research spanning the areas of neuroscience, visual science, cognitive science, artificial intelligence, mathematics, economics and social science, and computer engineering.

For further information:

Neil Rinaldi – Non Executive Director
E: nrinaldi@brainchip.com.au

PR
Ben Knowles
Walbrook Investor Relations
E: Ben.knowles@walbrookir.com.au
T: 61 426 277 760